

# Haible Living VCOs by **RANDOM\*SOURCE** - BOM

for **pcb versions 2015 and 2017** - BOM updated 3 Dec 2017

**All values for +/-15V operation - values in (round brackets) are for +/-12V**

<b>1 10p</b>	<b>C37</b>	C0G/NP0 recommended
<b>4 22p</b>	<b>C5A C5B C5C CM1</b>	C0G/NP0 recommended
<b>5 100p</b>	<b>CD27 CD30 C1 C12 C25</b>	C0G/NP0 recommended
<b>4 1nF</b>	<b>CD1 C2A C2B C2C</b>	C0G/NP0 recommended
<b>3 2n7</b>	<b>C3B C3Q C3S</b>	Polystyrene / Styroflex
<b>1 47n</b>	<b>CD31</b>	
<b>7 100n</b>	<b>CB1 CB2 CB3 CB4 CB5 CB6</b>	Bypass Caps - not (all) needed if SMT Bypass caps are installed
<b>1 100n</b>	<b>CD28</b>	C0G/NP0 recommended
<b>10 1uF</b>	<b>C6A C6B C6C CT2A CT2B CT2C CTA CTB CTC CD29</b>	Film (WIMA MKS2-5 or similar)
<b>2 10uF</b>	<b>C1P C2P</b>	POLARIZED CAPACITOR, 25V or higher rated
<b>6 22u</b>	<b>C7 C8 C15 C16 C27 C28</b>	POLARIZED CAPACITOR, 25V or higher rated
<b>23 100n</b>	<b>C3 C4 C9 C10 C11 C13 C18 C20 C21 C22 C23 C24 C30 C32 C33 C34 C35 C36 C38 C39 C40 C41 C42</b>	<b>BYPASS CAPS, X7R - most likely already installed</b>
<b>24 1N4148</b>	<b>D1A D1B D1C D2A D2B D2C D3A D3B D3C D4A D4B D4C D15A D15B D15C D16 D18 D19 DT1 DT2 DT3 DT4 DT5 DT6</b>	DIODE
<b>2 5V1</b>	<b>D17 D20</b>	ZENER DIODE 5.1V
<b>12 BC550C</b>	<b>Q3A Q3B Q3C Q4 Q4A Q4C Q5A Q5B Q5C Q7A Q7 Q7C</b>	NPN Transistor, <b>Q3, Q4:</b> <b>Rectangular pad = Emitter</b>
<b>12 BC560C</b>	<b>Q1A Q1B Q1C Q2A Q2B Q2C Q6A Q6B Q6C Q28 QD22 QD23</b>	PNP Transistor
<b>3 BC847C</b>	<b>Q9 Q9A Q9C</b>	IGNORE (SMT ALTERNATIVE TO Q7)
<b>6 BCM847DS</b>	<b>T1 T2 T4 T5 T7 T8</b>	<b>NXP BCM847 Transistor Array - BOTTOM SIDE!! USE BCM847DS OR THAT300 / MAT14 - NOT BOTH!!! ALTERNATIVE TO BCM847DS (T1, T2, T4, T5, T7, T8) - DO NOT USE BOTH!</b>
<b>3 THAT300S version 2015 only!</b>	<b>Q8A Q8B Q8C</b>	<b>ALTERNATIVE TO BCM847DS (T1, T2, T4, T5, T7, T8) - DO NOT USE BOTH!</b>
<b>3 MAT14 version 2017 only!</b>	<b>Q8A Q8B Q8C</b>	<b>ALTERNATIVE TO BCM847DS (T1, T2, T4, T5, T7, T8) - DO NOT USE BOTH!</b>
<b>3 PMP4201Y</b>	<b>QM QM1 QMC</b>	IGNORE (NXP PMP4201Y AS ALTERNATIVE TO Q4, Q5)
<b>3 SSM2212RZ</b>	<b>QSSMA QSSMB QSSMC</b>	IGNORE (\$\$\$ SMT ALTERNATIVE TO Q4, Q5)
<b>1 LM13700</b>	<b>UD1</b>	OTA
<b>3 LM311</b>	<b>IC2A IC2B IC2C</b>	
<b>7 OPA2134</b>	<b>IC1A IC1B IC1C IC7A IC7B IC7C (=IC7 IC10 IC12) UD5</b>	<b>Use OPA2134 as IC1x for better tracking!!</b>
<b>1 TL072</b>	<b>UD4</b>	or <b>OPA2134</b>

5 TL072 UD2 UD3 IC1 IC1A IC1C  
3 TL074 IC3A IC3B IC3C

3 1k tempco R11A R11B R11C

Tempco 3000ppm/K ...  
3500ppm/K - Mount on Top of  
Transistor Array (BCM847DS or  
THAT300/MAT14)

8 BEAD L1A L1B L1C  
L2A L2B L2C FD1 FD2

FERRIT BEAD

1 68R R121  
1 100R R119  
2 220R R127 R129  
1 300R R148  
6 330R R17A(=R49),R17B R17C  
R35A R35B R35C  
3 390R RS4B RS4C RS10  
18 1k R23A R23B R23C R27A R27B R27C  
R29A R29B R29C RA5X RA5Y RA5Z  
R131 R143 RM6 RA4A RA4 RA4C

3 1M(1M5) R39A R39C R39B

R39A and R39X are in series to allow  
easier adjustment of the **PULSE  
WIDTH RANGE** for a specific value.  
For 15V using 1M + 100R (or 10R)  
equals Jürgens original value.

**For 12V use 1M5 + link.**

See R39A ... Above

3 10R/LINK R39X R39Y R39Z  
1 1k5 R124  
3 2k(1k8) R12A R12B R12C  
3 2k(1k96) R25A R25B R25C  
7 2k2 RS1A RS1B RS1C RS6 RS6A RS22  
R123  
3 3k3 R19A R19B R19C  
3 5k1(4k7) R24 R24A R24C  
3 6.2k R30A R30B R30C  
35 10k R6A R6B R6C R16A R16B R16C  
R21A R21B R21C RA RB RC RS2A  
RS2B RS2C RS5 RS5A RS7 RS7A  
RS8A RS8B RS19 RS23 RS24 RT6A  
RT6B RT6C RT7A RT7B RT7C R147  
R149 R153 R154 R157  
3 12k(10k) R34A R34B R34C  
17 15k R20A R20B R26A R26B R26C R28A  
R28B R28C R31A R31B R31C R96  
RA1X RA1Y RA1Z R125 R156  
1 15k(10k) R150  
3 20k(47k) R22A R22B R22C  
21 20k RA1 RA1A RA1C RA2 RA2A RA2C  
RA2X RA2Y RA2Z RA3X RA3Y  
RA3Z RT1A RT1B RT1C RT2A  
RT2B RT2C RT5A RT5B RT5C

<b>3 20k</b> <b>(22k-27k)</b>	<b>R33A R33B R33C</b>
<b>1 22k</b>	<b>R130</b>
<b>3 51k</b>	<b>R10A R10B R10C</b>
<b>1 56k</b>	<b>R155</b>
<b>6 62k</b>	<b>RA3 RA3A RA3C RA4X RA4Y RA4Z</b>
<b>3 68k</b>	<b>R13A R13B R13C</b>
<b>19 100k</b>	<b>R4A R4B R4C RT8A RT8B RT8C</b> <b>R120 R122 R128 R137 R141 R144</b> <b>R151 R158 RM1 RM2 RM3 RM4</b> <b>RM5</b>
<b>11 100k .1%</b>	<b>R1A R1B R1C R2A R2B R2C R3A</b> <b>R3B R3C R145 R146</b>
<b>1 100k(75k)</b>	<b>R160</b>
<b>3 150k(120k)</b>	<b>R5A R5B R5C</b>
<b>3 150k(510k)</b>	<b>R38A R38B R38C</b>
<b>1 200k</b>	<b>R159</b>
<b>1 220k</b>	<b>R161</b>
<b>6 300k</b>	<b>RT3A RT3B RT3C</b> <b>RT4A RT4B RT4C</b>
<b>1 430k(150k)</b>	<b>R140</b>
<b>1 510k</b>	<b>R152</b>
<b>3 510k(390k)</b>	<b>R7A R7B R7C</b>
<b>3 750k(1M5)</b>	<b>R36A R36B R36C</b>
<b>12 1M</b>	<b>R15A R15B R15C R32A R32B R32C</b> <b>R126 R139 R142</b>
<b>3 1M(4.3M)</b>	<b>R37A R37B R37C</b>
<b>1 4.7M</b>	<b>R138</b>
<b>3 10M</b>	<b>R14A R14B R14C</b>
<b>Trimmer:</b>	
<b>3 LINK or 50K</b>	<b>TRPA TRPB TRPC</b> <b>(=R40T1 R40T2 R40T3)</b>
<b>1 10k</b>	<b>VCA-CV - not 100k as in previous BOM1</b>
<b>9 100k</b>	<b>OCT_DOWN OCT_UP RND RND1</b> <b>RND2 TRI-LEVEL TRI-LEVEL1 TRI-LEVEL2 VCA</b>
<b>3 100k</b>	<b>TR8-FREQ TR8-FREQA TR8C-FREQ</b>
<b>3 10k</b>	<b>TR9A-SCALE TR9B-SCALE TR9C-SCALE</b>
<b>3 LINK or 2K</b>	<b>TR18-HF TR18-HF1 TR18-HF2</b>
<b>3 50k</b>	<b>SYMMETRY SYMMETRY1</b> <b>SYMMETRY2</b>

**12V:** value determines PW- Knob behavior at min (CCW): 22k means a thin pulse, 27k means too thin = silence. Great for rhythmic effects as 0V CV can be used to mute the Pulse output. Pick according to taste.

Resistors, 0.1% - you can also use 1% and match them in pairs or triples. You should match all resistors that go from CV inputs to one node, like R1A, R2A, R3A. Similarly R1B, R2B, R3B etc.. Finally, R145, R146. There is no need to match between these groups.

**12V: updated value for PWM**

**12V: updated value for PWM**

**Check size of footprints for trimmers to see if they fit!**

Omit trimmers for PWM and use link / jumper (pins 2-3) instead

Trimpot (3362P) or small Multiturn

Trimpot (3362P) or small Multiturn

Multiturn-Trimpot S64YW

Multiturn-Trimpot S64YW

**R\*S mod: use link between pin 1 and 2 (center) and OPA2134 as IC1x2P) or small Multiturn**

Trimpot (3362P) or small Multiturn